

JCWS/CS 20 AUG 2004

AUG. 19. 2004 2:30PM Haynes Beffel & Wolfeld LLP

No. 4371 P. 1

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INTELLECTUAL PROPERTY LAW

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FACSIMILE TRANSMITTAL FORM

FAX NUMBER:	(703) 746-9195
TRANSMITTED TO:	Office of Initial Patent Examination, Filing Receipt Corrections
OF:	United States Patent and Trademark Office
FROM:	Mark A. Haynes, Reg. No. 30,846
CLIENT/MATTER:	MXIC 1520-1 (SN 10/699,764)
DATE:	19 AUGUST 2004

TOTAL NUMBER OF PAGES INCLUDING THIS COVER SHEET: 10

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Application No. 10/699,764

MXIC 1520-1
(P900385)**CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8**

I hereby certify that this correspondence is being facsimile transmitted to the Office of Initial Patent Examination, Filing Receipt Corrections, United States Patent and Trademark Office at Fax No. (703) 746-9195 on 19 August 2004.

Kathryn Marley

*Kathryn Marley***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application
 Inventor(s): **Albert Sun et al.**
 Appl. No.: **10/699,764**
 Confirm. No.: **4234**
 Filed: **3 November 2003**
 Title: **In-Circuit Configuration Architecture for
Embedded Configurable Logic Array**

Group Art Unit: 2183
 Examiner: unknown

Customer No. 22470

Office of Initial Patent Examination
 Filing Receipt Corrections
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Sir:

Enclosed is a copy of the official Filing Receipt marked to identify the correction needed. The correction is as follows:

In the Applicant(s), after "Shih-Liang Chen," please delete Hsinchu and insert -- Fengyuan --.

Although the Declaration indicated that this inventor's address was in Hsinchu, the Application Data Sheet shows the correct address in Fengyuan. Attached for convenience is a copy of the Application Data Sheet filed with the application.

It is believed that no fee is due. However, the Commissioner is hereby authorized to charge any fees which may be required by this paper, or to credit any overpayment to Deposit Account No. 50-0869 (MXIC 1520-1).

Respectfully submitted,

Dated: August 19, 2004

Mark A. Haynes
Mark A. Haynes, Reg. No. 30,846

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MXIC 1520-1
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/699,764	11/03/2003	2183	986	MXIC 1520-1	7	32	3

CONFIRMATION NO. 4234

22470
 HAYNES BEFFEL & WOLFELD LLP
 P O BOX 366
 HALF MOON BAY, CA 94019

FILING RECEIPT



OC000000013467613

Date Mailed: 08/06/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Albert Sun, Hsinchu, TAIWAN;
 Eric Sheu, Hsinchu, TAIWAN;
 Shih-Liang Chen, Hsinchu, TAIWAN;

HAYNES BEFFEL & WOLFELD LLP

AUG 09 2004

Assignment For Published Patent Application

MACRONIX INTERNATIONAL CO., LTD., HSINCHU, TAIWAN;

RECEIVED

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 08/06/2004

Projected Publication Date: 05/05/2005

Non-Publication Request: No

Early Publication Request: No

Title

In-circuit configuration architecture for embedded configurable logic array

Preliminary Class

712

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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